

## Microwave dielectric loss at single photon energies and millikelvin temperatures

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The microwave performance of amorphous dielectric materials at very low temperatures and very low excitation strengths displays significant excess loss. Here, we present the loss tangents of some common amorphous and crystalline dielectrics, measured at low temperatures ( $T < 100$  mK) with near single-photon excitation energies,  $E/\hbar\omega_0 \sim 1$ , using both coplanar waveguide and lumped  $LC$  resonators. The loss can be understood using a two-level state defect model. A circuit analysis of the half-wavelength resonators we used is outlined, and the energy dissipation of such a resonator on a multilayered dielectric substrate is theoretically considered. © 2008 American Institute of Physics. [DOI: 10.1063/1.2898887]

Dielectric loss is a significant concern for superconducting quantum bits (qubits), as energy relaxation within the dielectric is one of the primary sources of quantum decoherence.<sup>1</sup> Superconducting qubits operate in the low-temperature, low-voltage regime, where dielectric loss is typically not well characterized. While the dielectric loss may be extremely small at higher excitation voltages and temperatures, it has been observed that the loss tangent scales inversely with voltage ( $\tan \delta \sim 1/V_{\text{rms}}$ ) and levels off at an intrinsic value  $\tan \delta_i$  that is often substantially greater than the loss at larger voltages, as shown in Fig. 1. The lowest excitation voltages shown there correspond to order 1 photon in a 6 GHz  $LC$  resonator, with  $C \sim 1$  pF.

This behavior has been postulated to arise from coupling to a bath of two-level state (TLS) defects in the dielectric, which absorb and disperse energy at low power but become saturated with increasing voltage and temperature.<sup>1,2</sup> Thus, a signature of TLS-induced loss is the observed increase in loss with decreasing excitation voltage. TLS are found in most amorphous materials and arise from an energy difference between defect bond configurations coupled by tunneling.<sup>3,4</sup> The bath of TLS is assumed to have a constant distribution in energy and a log uniform distribution in transition strength.<sup>5</sup> These defect states couple to the surrounding electric field through the electric dipole moments that arise from differences in the charge distribution between configurations.<sup>6</sup>

Although dielectric loss at higher powers and temperatures has been extensively reported,<sup>7</sup> the literature contains very little information on dielectric performance in the low-temperature, low-voltage limit. Guided by prior measurements of hydrogenated dielectrics with over-constrained lattices,<sup>8</sup> we examined the microwave loss of a range of dielectric materials compatible with qubit fabrication. Here, we report direct measurements of the intrinsic loss tangents of these dielectric materials.

To perform these measurements, we fabricated both parallel  $LC$  resonators, comprising of a superconducting inductive coil and a parallel-plate capacitor containing the dielectric in question, and half-wavelength coplanar waveguide

(CPW) resonators, where the single-layer superconducting metal electrodes are patterned atop the dielectric. A CPW resonator is shown in Fig. 2(a).  $LC$  resonators afford more straightforward analysis of the loss tangent, due to the parallel electric field configuration between the plates, while CPW resonators are easier to fabricate, but require more complicated analysis. Both types of resonators were coupled to measurement lines through on-chip coupling capacitors  $C_c$ , as illustrated in Fig. 2(b). The resonators had resonance frequencies near the 6 GHz operating frequencies of our qubits. The resonators' transmission  $S$ -parameter  $S_{21}$  was measured as a function of voltage and temperature, using a vector network analyzer. The loss tangents were extracted as described below. The results of these measurements are compiled in Table I.

Near its half-wave resonance frequency, a CPW resonator can be represented by an equivalent  $LC$  lumped circuit, shown in Fig. 2(b). The Norton equivalent circuit is shown in Fig. 2(c), where the voltage source has been transformed to a current bias  $V_1/(R_0+Z_c) \approx V_1/Z_c$ , and the impedance  $R_0+Z_c$  can be written as  $Z_c \parallel |Z_c|^2/R_0$ , where we have used  $|Z_c| = 1/\omega C_c \gg R_0$  for typical coupling capacitances  $C_c$  on the order of a few femtofarads. This can now be viewed as a parallel  $LCR$  circuit with effective capacitance  $C' = C + 2C_c$  and

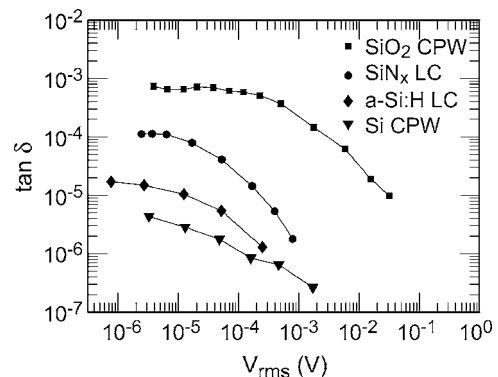


FIG. 1. Loss tangent after adjusting for the electrical loading. Data labeled SiO<sub>2</sub> and Si correspond to 300 nm plasma-enhanced chemical vapor deposition (PECVD) SiO<sub>2</sub> on single-crystal Si, and 100  $\Omega$ -cm single-crystal Si, respectively. All resonators had Al electrodes. Measurements made with  $T \leq 100$  mK.

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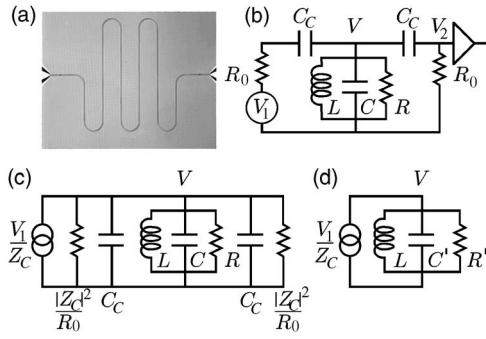


FIG. 2. (a) Micrograph of a half-wavelength CPW resonator. (b) Circuit representation and measurement lines. (c) Norton equivalent circuit ( $|Z_c| \gg R_0$ ). (d) LCR equivalent circuit.

resistance  $R' = R \parallel |Z_c|^2 / 2R_0$  [Fig. 2(d)]. The response at frequency  $\omega$ , near the resonance frequency  $\omega_0 = 1/\sqrt{LC'}$ , is given by

$$V = \frac{V_1}{Z_c} \left( \frac{1}{1/R' + 1/i\omega L + i\omega C'} \right). \quad (1)$$

The output voltage  $V_2$ , as shown in Fig. 2(b), is given by  $V_2 = VR_0/(R_0 + Z_c) \approx VR_0/Z_c$ . The normalized scattering matrix parameter is given by  $S_{21} = 2V_2/V_1$ , where we have used  $|S_{21}| = 1$  for the on-resonance transmittance of a lossless resonator. Finally, taking  $Q_m = R'/\omega_0 L$ ,  $R_c = |Z_c|^2 / 2R_0$ ,  $Q_m \gg 1$ , and  $\omega \approx \omega_0$ , we obtain

$$S_{21} \approx - \frac{1}{1 + R_c/R} \left( \frac{1}{1 + i2Q_m(\omega - \omega_0)/\omega_0} \right). \quad (2)$$

This equation is used to fit our measured  $S_{21}$  data [see Fig. 3(a)], from which we can extract the total measured quality factor  $Q_m = 1/\tan \delta$ . The quality factor is attributed to the parallel sum of two independent loss mechanisms,  $1/Q_m = 1/Q_0 + 1/Q_c$ , where  $1/Q_0$  is the internal dielectric loss, and  $1/Q_c$  the loss due to the measurement impedance  $R_0$ . We calculate  $Q_c$  either from the formula  $1/Q_c = 2R_0 Z_0 \omega_0^2 C_c^2$ , where  $Z_0$  is the resonator characteristic impedance, or through the relation  $Q_c = Q_m/|S_{21}|$  for over-coupled samples, when  $Q_m$  saturates at high powers and  $|S_{21}| \approx 1$ . Finally, the limiting loss tangent is related to  $Q_0$  at the lowest excitation voltage,  $\tan \delta_0 = 1/Q_0$ .

TABLE I. Intrinsic loss tangents, after accounting for external loss and CPW field-distribution analysis. Deposited films have typical thickness of a few hundred nanometers. Materials marked "SC" indicate single crystals.

Dielectric	Metal	Resonator	$\tan \delta_i \times 10^6$
100 $\Omega$ -cm Si (SC)	Al	CPW	<5–12
Sapphire (SC)	Re	CPW	<6–10
Sapphire (SC)	Al	CPW	<9–21
$\alpha$ -Si:H	Al	LC	22–25
$\alpha$ -Si:H	Al	CPW	10–130
Interdigitated cap. on sapphire (SC)	Al	LC	41–47
$\text{SiN}_x$	Re or Al	LC or CPW	100–200
Thermal $\text{SiO}_2$	Al	CPW	300–330
Sputtered Si	Al	CPW	500–600
AlN	Al	CPW	1100–1800
PECVD $\text{SiO}_2$	Al	CPW	2700–2900
MgO	Al	CPW	5000–8000

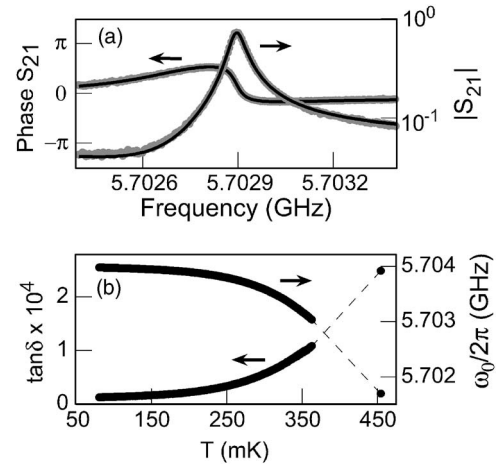


FIG. 3. (a) Measured  $S_{21}$  for an Al/100  $\Omega$ -cm Si CPW resonator (phase and magnitude, gray points) and the corresponding Lorentzian fit using Eq. (2) (black line). Resonator was measured at 86 mK with excitation  $V_{\text{rms}} = 1$  mV. (b) Temperature dependence of a resonator with the same construction as that in (a), measured with excitation  $V_{\text{rms}} = 1$  mV.

For an LC resonator, this limiting loss tangent is a direct measurement of the low-power, low-temperature intrinsic loss of the dielectric,  $\tan \delta_i$ . This can be seen by noting that the electric field in an LC resonator is almost entirely confined to the space between the capacitor plates. Furthermore, the inductive loss is typically negligible at these temperatures.<sup>9</sup> However, in a CPW resonator, the electric field samples a large volume of space around the CPW not filled by the dielectric of interest, so the limiting loss tangent  $\tan \delta_0$  is not identical to the intrinsic loss tangent. For a CPW resonator fabricated on a multilayer substrate, it is necessary to know the fraction of the electrical energy stored in each dielectric, and the intrinsic loss tangents for all but one of the constituent dielectrics, as well as the value of limiting loss tangent for the composite structure.

This can be seen by considering the quality factor of a resonator driven at frequency  $\omega$ , defined as  $Q = \omega(W_m + W_e)/P_l$ , where  $W_m$  and  $W_e$  are the time-averaged magnetic and electric energies stored in a given volume, respectively, and  $P_l$  is the time-averaged power dissipated in that volume.<sup>10</sup> For a resonator driven on resonance,  $\omega = \omega_0$  and  $W_m = W_e$ , so that  $Q = 2\omega_0 W_e/P_l$ . Furthermore,  $P_l$  can be expressed as  $P_l = \frac{1}{2}\omega_0 (\text{Im} \int \epsilon |\mathbf{E}|^2 d^3x + \text{Im} \int \mu |\mathbf{H}|^2 d^3x)$ , where  $\epsilon$  is the spatially varying complex dielectric constant. Ignoring magnetic loss, which we do not believe to contribute significantly, this reduces to  $P_l = \frac{1}{2}\omega_0 \text{Im} \int \epsilon |\mathbf{E}|^2 d^3x$ . With  $W_e = \frac{1}{4} \text{Re} \int \epsilon |\mathbf{E}|^2 d^3x$ , we can re-express the resonant quality factor as

$$Q = \frac{\text{Re} \int \epsilon |\mathbf{E}|^2 d^3x}{\text{Im} \int \epsilon |\mathbf{E}|^2 d^3x}. \quad (3)$$

It is useful to consider the time-averaged electric energy divided by the quality factor,

$$\frac{W_e}{Q} = \frac{1}{4} \text{Im} \int \epsilon |\mathbf{E}|^2 d^3x. \quad (4)$$

This is a general expression for a spatially varying dielectric constant. In our structures, the total volume can be divided into distinct isotropic regions.

For example, for a CPW resonator formed by patterned Al on 300 nm SiO<sub>2</sub> that was commercially grown in a furnace on a 100 Ω-cm single-crystal Si substrate, we separate Eq. (4) into two parts,  $W_e/Q_0 = \frac{1}{4} \text{Im} \int_A \epsilon_A |\mathbf{E}_A|^2 d^3x + \frac{1}{4} \text{Im} \int_B \epsilon_B |\mathbf{E}_B|^2 d^3x$ , where the volumes *A* and *B* correspond to the regions occupied by the SiO<sub>2</sub> and the Si, respectively. This can be re-written as  $W_e/Q_0 = W_{eA}/Q_A + W_{eB}/Q_B$ , or in terms of the intrinsic loss tangents  $\tan \delta_{i,A}$  and  $\tan \delta_{i,B}$  as

$$W_e \tan \delta_0 = W_{eA} \tan \delta_{i,A} + W_{eB} \tan \delta_{i,B}. \quad (5)$$

We now proceed to extract the intrinsic loss of the SiO<sub>2</sub>. A finite-element analysis of the electric field distribution shows 27% of the total time-averaged energy is stored in the SiO<sub>2</sub>, 61% in the Si, and the remainder in the vacuum. The limiting loss tangent of this CPW resonator was measured to be  $8.9 \times 10^{-5}$ . The intrinsic loss tangent for single-crystal silicon was extracted from the analysis of a CPW resonator on 100 Ω cm Si, yielding  $\tan \delta_{i,\text{Si}} = 4.8 \times 10^{-6}$ . We thus find that the intrinsic loss tangent of thermal SiO<sub>2</sub> is  $3.2 \times 10^{-4}$ . In this fashion, we extracted the intrinsic loss tangents of all the dielectrics measured with CPW resonators, as tabulated in Table I.

As expected, thermal SiO<sub>2</sub> exhibits comparatively high loss.<sup>11</sup> The results of Table I imply that a more highly constrained lattice is correlated to lower loss. This can be seen in the silicon compounds where the transition from SiO<sub>2</sub> → SiN<sub>x</sub> → *a*-Si:H → single-crystal Si corresponds to an increase in coordination number and a decrease in loss. Furthermore, the lower bounds on single-crystal Si and sapphire are not precisely known, because the measurements may be limited by factors other than dielectric loss, such as radiation. However, fabricating devices with single-crystal dielectrics is more difficult than using easily deposited amorphous materials. Due to this, we are currently optimizing the deposition of *a*-Si:H since it is the least lossy amorphous material, and in general, the loss tangent has been seen to correlate to the coherence times in our phase qubits.<sup>12</sup>

These measurements were all taken at temperatures near 100 mK. At higher temperatures, the dielectric loss may be overshadowed by the loss in the superconducting Al electrodes.<sup>13</sup> In Fig. 3(b), we display the temperature-

dependent loss of an Al/100 Ω-cm Si CPW. The higher loss with increasing temperature, and the frequency shift, are consistent with other measurements.<sup>9,14</sup>

In conclusion, we have reported the low voltage, low temperature, intrinsic loss of many dielectrics. Furthermore, we have shown how to extract the intrinsic dielectric loss from CPW resonator data and find the results of measured CPW resonators to be commensurate with values given by LC resonators. Discovering other materials with lower loss tangents than the dielectrics reported here would offer significant improvements in qubit coherence times, and may be a crucial step in developing a scalable superconducting quantum computer.

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